

Notice of Allowability

Application No.

10/691,382

Examiner

Jasmine J Clark

Applicant(s)

OGAWA, TSUYOSHI

Art Unit

2815

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 09/10/04.
2. ☒ The allowed claim(s) is/are 1-3 and 7-10.
3. ☒ The drawings filed on 22 October 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

JASMINE CLARK
PRIMARY EXAMINER

Jasmine Clark

Election

1. Applicant's election of group I, claims 1-3, and 7-10 in the reply filed on 09/10/04 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).

EXAMINER'S AMENDMENT

2. This application is in condition for allowance except for the presence of claims 4-6, and 11-13 non-elected without traverse. Accordingly, claims 4-6, and 11-13 have been cancelled.

3. Leib et al. (US 2004/0214380 A1), Fukumura et al. (US 6,121,675), Schwarzrock et al. (US 6,605,828 B1), Suzuki (US 5,362,976), Hsu et al. (US 6,617,702 B2), teach a device comprising a substrate with a semiconductor chip which is an optical chip disposed on the substrate. However, none of the reference(s) teaches as follows:

1) an electric chip having connection terminals connected to connection lands, and mounted on the first surface of the insulating layer with the connection terminals connected the connection lands for processing an electric signal inputted and/or outputted through the interlayer via: and an optical chip having a terminal section connected through the interlayer via to the connection land with the connection terminal connected thereto, having a light emitting section and/or a photo detecting section formed on a surface thereof and buried in the insulating layer with the surface coinciding

Art Unit: 2815

with a second surface of the insulating layer so that the light emitting section and/or photo detecting section is exposed on the second surface of the insulating layer as claimed in the claimed invention.

Allowable Subject Matter

4. Claims 1-3, and 7-10 are allowed.

The following is an examiner's statement of reasons for allowance: please see paragraph 3 above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Search

5. This application was searched, in class 257, subclasses 81, 99, 431, 432, 701, 702, 723, 778, 774, E33.076, E31.105, E31.106, E31.127, and E25.032 (photo adj diode LED light adj emitting adj diode optical adj (chip die IC)), in view of the disclosure here. This search was ineffective.

Telephone Inquiry Contacts

Art Unit: 2815

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jasmine J Clark whose telephone number is (571) 272-1726. The examiner can normally be reached on Flex.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jjbc/11/18/04